



Analog.7020

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**APPLICANT:** Frederic Boutaud **GROUP:** 2186  
**SERIAL NO:** 10/715,629 **EXAMINER:** Unknown  
**FILED:** November 17, 2003  
**FOR:** DIGITAL SIGNAL PROCESSOR ARCHITECTURE WITH  
OPTIMIZED MEMORY ACCESS

Hon. Commissioner of Patents  
and Trademarks  
Washington, D.C. 20231

Sir:

**PETITION AND FEE FOR EXTENSION OF TIME**

Pursuant to 37 CFR 1.136(a), petition is made for a two (2 ) month extension of time to file the missing parts which were due April 13, 2004, the missing parts now being due June 13, 2004. Enclosed is a check in the amount of \$420.00 for the extension fee. The Commissioner is authorized to charge any further extension and/or fee that is required to Deposit Order Account 19-0079.

Respectfully submitted,

Matthew E. Connors  
Registration No. 33,298  
Gauthier & Connors LLP  
225 Franklin Street, Suite 3300  
Boston, Massachusetts 02110  
Tel. No. (617) 426-9180, Writer's Ext.:

I hereby certify that this correspondence (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, Mail Stop Missing Parts

Deborah M. Costello

Date

05/26/2004 JBALINAN 00000034 10715629

01 FC:1252

420.00 DP

5/20/04